

## Electronic Patent Application Fee Transmittal

<b>Application Number:</b>	10814968			
<b>Filing Date:</b>	31-Mar-2004			
<b>Title of Invention:</b>	Integrated circuit having a memory cell array capable of simultaneously performing a data read operation and a data write operation			
<b>First Named Inventor/Applicant Name:</b>	Kyo-Min Sohn			
<b>Filer:</b>	Frank Chau/marg dougan			
<b>Attorney Docket Number:</b>	8021-225JHM/SS18860US			
Filed as Large Entity				
<b>Utility      Filing Fees</b>				
<b>Description</b>	<b>Fee Code</b>	<b>Quantity</b>	<b>Amount</b>	<b>Sub-Total in USD(\$)</b>
<b>Basic Filing:</b>				
<b>Pages:</b>				
<b>Claims:</b>				
<b>Miscellaneous-Filing:</b>				
<b>Petition:</b>				
<b>Patent-Appeals-and-Interference:</b>				
<b>Post-Allowance-and-Post-Issuance:</b>				
Statutory disclaimer	1814	1	130	130
<b>Extension-of-Time:</b>				

Description	Fee Code	Quantity	Amount	Sub-Total in USD(\$)
Miscellaneous:				
Total in USD (\$)				130